



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Eric M. Dowling
App. No. : 10/074,779
Filed : February 13, 2002
For : **EMBEDDED-DRAM-DSP
ARCHITECTURE**
Examiner : Huisman, David J.
Group : 2183

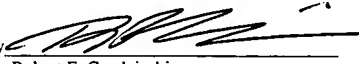


27299

PATENT, TRADEMARK OFFICE

I hereby certify that this paper is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 CFR 1.10 on the date indicated below and is addressed to Mail Stop RCE, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450

Express Mail Label No. EV 520323556 US
Date of Deposit: Tuesday, January 24, 2006

By 
Robert F. Gazdzinski
Reg. No. 39,990

5

AMENDMENT AND RESPONSE TO FINAL OFFICE ACTION
(INCLUDING REQUEST FOR CONTINUED EXAMINATION)

10

Mail Stop RCE
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

15

Dear Sir:

In response to the Final Office Action dated July 25, 2005 ("Office Action"), the following is provided: